

FIG.1

1

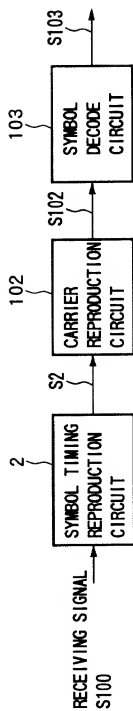


FIG. 2

2 SYMBOL TIMING REPRODUCTION CIRCUIT

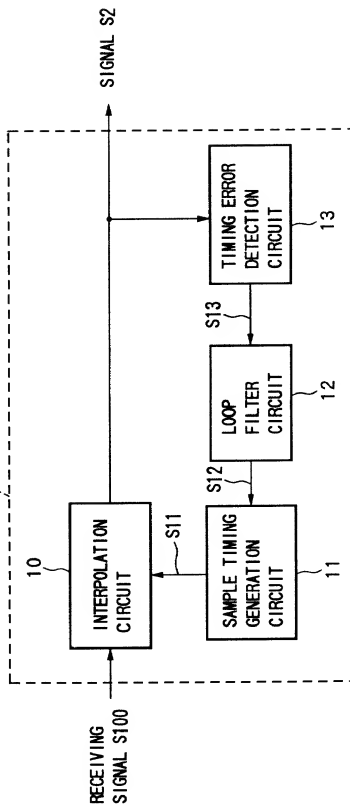


FIG.3A

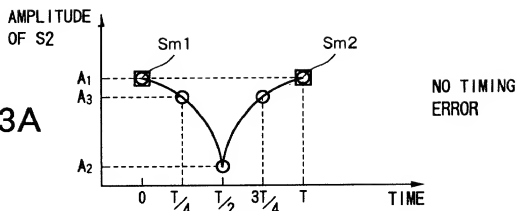


FIG.3B

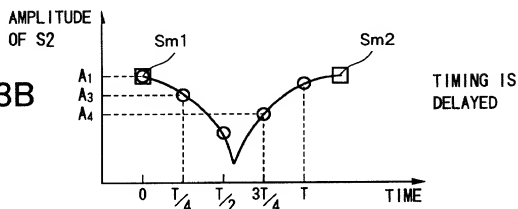
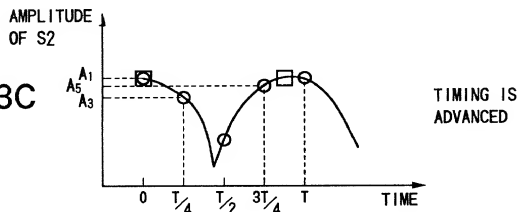


FIG.3C



- SYMBOL OF S2
- SAMPLING POSITION IN TIMING ERROR DETECTION CIRCUIT

FIG.4

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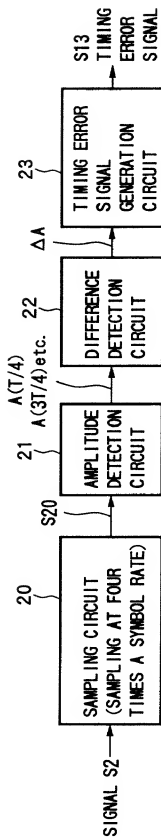


FIG.5

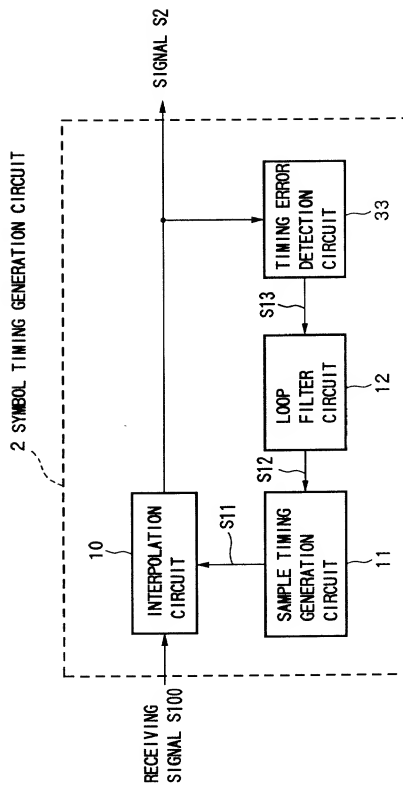
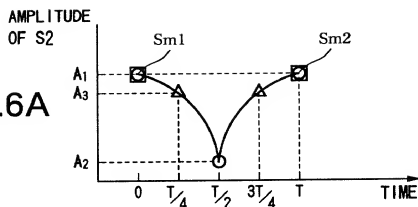
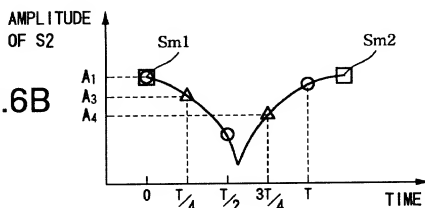


FIG.6A



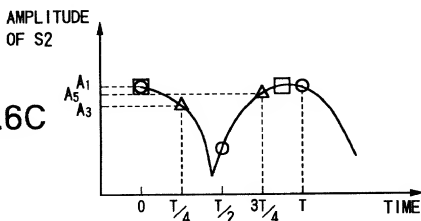
NO TIMING ERROR

FIG.6B



TIMING IS DELAYED

FIG.6C



TIMING IS ADVANCED

- SYMBOL OF S2
- SAMPLING POSITION IN
TIMING ERROR DETECTION CIRCUIT
- △ DATA OBTAINED BY INTERPOLATION PROCESSING

FIG. 7

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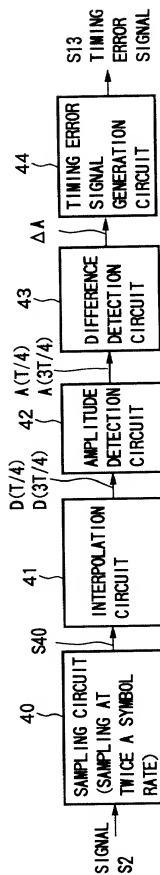
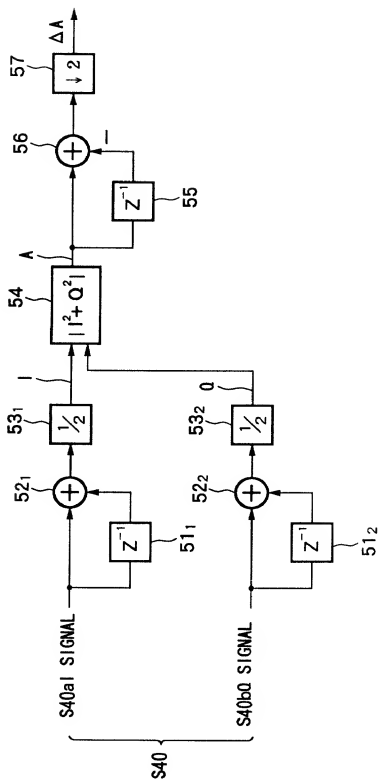


FIG. 8



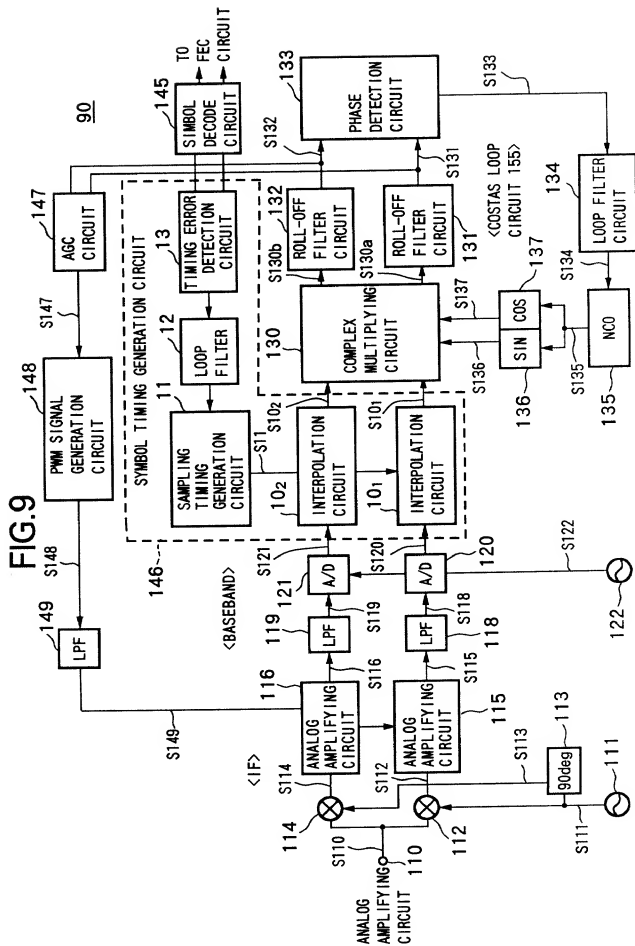


FIG.10

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